

### **AMENDMENTS TO THE SPECIFICATION**

Please amend the specification by replacing the paragraph on Page 1 of the specification titled "Cross-Reference to Related Applications", with the following corrected paragraph.

#### **Cross-Reference to Related Applications**

This application is related to the following issued patent and co-pending U. S. Applications:

U. S. Patent No. 6,946,985 B2 issued Sept. 20, 2005 titled " Device for Reconfiguring a Faulty Storage Assembly."

U. S. patent application Ser. No. ~~10/268,686~~ 10/286,686 filed November 1, 2002, titled "Reconfiguration Device for a Faulty Memory."

U. S. patent application Ser. No. 10/379,126 filed March 3, 2003, titled "~~A Reconfiguration Device for a Faulty Memory.~~" "Programmable Test for Memories."

U. S. patent application Ser. No. 10/650,066 filed August 8, 2003, titled "Evaluation of the Characteristics of Electric Pulses."

U. S. patent application Ser. No. 10/492,294 filed April 9, 2004, titled "Circuit Architecture Protected Against Perturbations."

U. S. patent application Ser. No. 10/494,080 filed July 6, 2004, titled "Data Storage Method with Error Correction."